

Notice of References Cited

Application/Control No.

10/694,822

Applicant(s)/Patent Under
Reexamination
FUJII ET AL.

Examiner

Brian P. Johnson

Art Unit

2183

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,414,368	07-2002	May et al.	257/523
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
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	K	US-			
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FOREIGN PATENT DOCUMENTS

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	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Chip Layout Optimization Using Critical Path Weighting
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